

Amendments to the Specification:

Amend the title as follows:

LOW POWER SELF-BIASING OSCILLATOR CIRCUIT

Replace the paragraph beginning at line 18, page 2, with the following paragraph:

The self-bias circuit 106 has transistors  $M_{n1}$ ,  $M_{p1}$ ,  $M_{n2}$ , and  $M_{p2}$  and a resistor  $R_{bias}$  connected to form a constant biasing circuitry for providing a relatively constant bias current and voltage.  $M_{p1}$ ,  $M_{n1}$ , and  $R_{bias}$  are connected in series and form one leg of the constant biasing circuitry.  $R_{bias}$  provides a negative feedback in response to a change in the amount of current  $I_1$  flowing through  $M_{n1}$  and  $M_{p1}$ .  $M_{p2}$  and  $M_{n2}$  are also connected in series and form another leg of the constant biasing circuitry. The gate nodes of  $M_{p1}$  and  $M_{p2}$  are connected through node PBIAS, and the gate nodes of  $M_{n1}$  and  $M_{n2}$  are connected through node VBIAS. The drain and gate nodes of  $M_{n2}$  are connected, and the drain and gate nodes of  $M_{p1}$  are connected. This arrangement produces a relatively constant current  $I_1$  flowing through  $M_{p1}$  and  $M_{n1}$ , and a relatively constant current  $I_2$  flowing through  $M_{p2}$  and  $M_{n2}$ .